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U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
SCANNED <u>A.C.4</u> O.A. <u>1/1</u>	

APPLICATION NO. 09/964472	CONT/PRIOR	CLASS 438	SUBCLASS 149	ART UNIT 2812	EXAMINER C. A. H.
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Method of making high-voltage bipolar/CMOS/DMOS (BCD) devices

PTO-2040
1300

ISSUING CLASSIFICATION

Continued on Issue Slip Inside File Jacket

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
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<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				Amount Due	Date Paid
				ISSUE BATCH NUMBER	

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